Calculating Packets Interarrival Times using Hashes and Registers

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The Cyberinfrastructure Lab at UofSC Energy Sciences Network (ESnet) National Science Foundation (NSF)

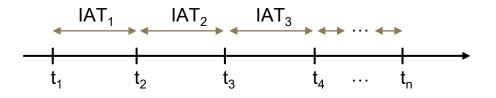
Internet2 Technology Exchange

Monday December 5th, 2022 Denver, Colorado

Hands on Session 2: Calculating Packets Interarrival Times using Hashes and Registers

Interarrival Times

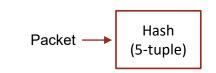
- The Interarrival time (IAT) is the time between two consecutive packets belonging to the same flow
- Calculating IAT is not possible on a general-purpose CPU, especially when the traffic rate is high
- Programmable switches offer a granular timestamp that can be leveraged to compute the IAT of packets



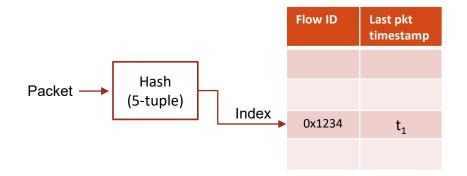
Hashing in P4

- The Interarrival time (IAT) is the time between two consecutive packets belonging to the **same flow**
- A flow can be identified by its 5-tuple fields:
 - Source IP address
 - Destination IP address
 - Source port
 - Destination port
 - Protocol
- It is possible to hash multiple fields in P4 and get a single digest value
- Cyclic Redundancy Check (CRC) is an example of a hash algorithm provided by the v1model

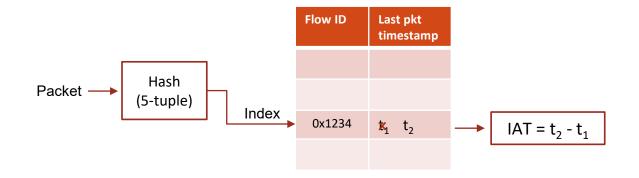
IAT Calculation in P4



IAT Calculation in P4



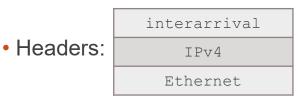
IAT Calculation in P4

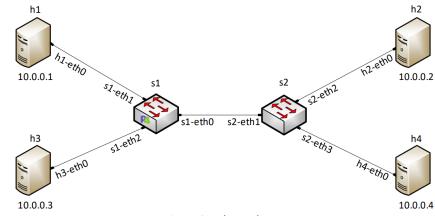


Lab 10: Calculating Packets Interarrival Times using Hashes and Registers

Lab Topology and Objectives

- The topology consists of four hosts: h1, h2, h3 and h4; one P4 switch: s1; one legacy switch
- The hosts h1 and h3 send packets with a custom header (interarrival)
- A flow is identified by the source IP and the destination IP of the packet
- The P4 switch s1 computes the IAT and inserts its value into the custom header
- Hosts h2 and h4 receive packets from hosts h1 and h3, respectively
- The IATs are shown on hosts h2 and h4





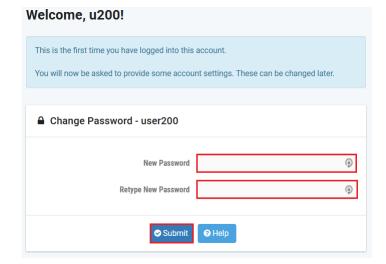
Accessing the Platform

- Please use the following link to access the platform:
 - <u>https://netlab.cec.sc.edu/</u>
- Login using your credentials





Cyberinfrastructure Lab @ UofSC



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			Time Zone	(GMT-05:00) Eastern Time (US & Canada) -
	➡ Change E-mail Address		Date Display Format	YYYY-MM-DD (2016-09-15)
			Time Display Format	24 Hour (15:37) 👻
	E-mail Address testuser@example.edu		First Day of Week	Sunday 👻
	Submit Help			Submit Help

- Click on New Lab Reservation
- Click on Schedule Lab for Myself

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Internet2 Workshop 2 - P4 Programmable Data Planes	Jorge Crichigno, Jose Gomez, Elie Kfoury, Ali Sabeh	None	None	
Show 25 - entries Showing 1 to 2 of 2 it	tems		< 1 >	

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- Select the course
- For this session, we will use "P4 Applications and Custom Processing"

Multiple course topics are available. Please select one.

Intro. to P4 Programmable Data Planes Introduction to P4 programmable data planes with BMv2

P4 Applications and Custom Processing

This lab series presents P4 applications, stateful elements, and custom packet processing

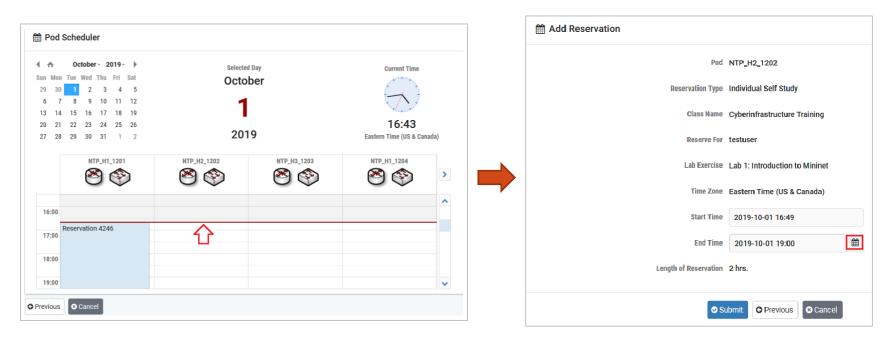
- Select the Lab
- For this session, we will run:
 - Lab 10: Calculating Packets Interarrival Times using Hashes and Registers

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Lab Name	
Lab 1: Introduction to Mininet	
Lab 2: Introduction to P4 and BMv2	
Lab 3: P4 Program Building Blocks	
Lab 4: Defining and Processing Custom Headers	
Lab 5: Monitoring the Switch Queue using Standard Metadata	
Lab 6: Collecting Queueing Statistics using a Header Stack	
Lab 7: Measuring Flow Statistics using Direct and Indirect Counters	
Lab 8: Rerouting Traffic using Meters	
Lab 9: Storing Arbitrary Data using Registers	
Lab 10: Calculating Packets Interarrival Times using Hashes and Registers	
Lab 11: Generating Notification Messages from the Data Plane using Digests	

This lab series presents P4 applications, stateful elements, and custom packet processing

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Select the next available POD and allocate time



Website URL and Accessing the Platform

• Tutorial website with slides and URL to resources:

http://ce.sc.edu/cyberinfra/workshop_2022_ie2_p4.html

• Access to virtual platform for this tutorial:

https://netlab.cec.sc.edu/