Sergio J. Elizalde F.

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Birth: 06/28/1997

EDUCATION

Catholic University Asuncion Electronic Engineer –Department of Electronics and Computer Science 2016–2023 - Thesis: "Implementation of Human Activity Recognition algorithm in a neuroprosthetic device". Advisor: Ph.D. Fernando Brunetti Columbia University of South Carolina Columbia PhD in Informatics –Department of Integrated Information Technology 2024–Current - Expected end date 2028 EXPERIENCE

Catholic University

Junior Researcher/ Digital Electronics Laboratory

- Keywords: Neurorehabilitation, FES, Neuroprosthetics, Experts systems
- Design experiments to measure biomechanics of human movement during locomotion activies. Development of real time human recognition algorithms. Implementation of Finite States Machines in ARM microcontrollers.

TECHNICAL SKILLS

- **Programming/Scripting:** Matlab, C, Python, VHDL.
- Technologies: ARM microcontrollers, IoT, Biosignal Amplifiers.

Courses

•	Data Analyst Course	2022
	UCOM University. Duration: 100 Hours	
•	Cisco Networking Academy Connecting Things course - IoT Fundamentals	2021
	Universitat Politécnica de Valencia - UPV, Spain. Duration: 50 hours	



Asuncion August 2022–Current