A Hands-on Workshop on P4 Programmable Switches

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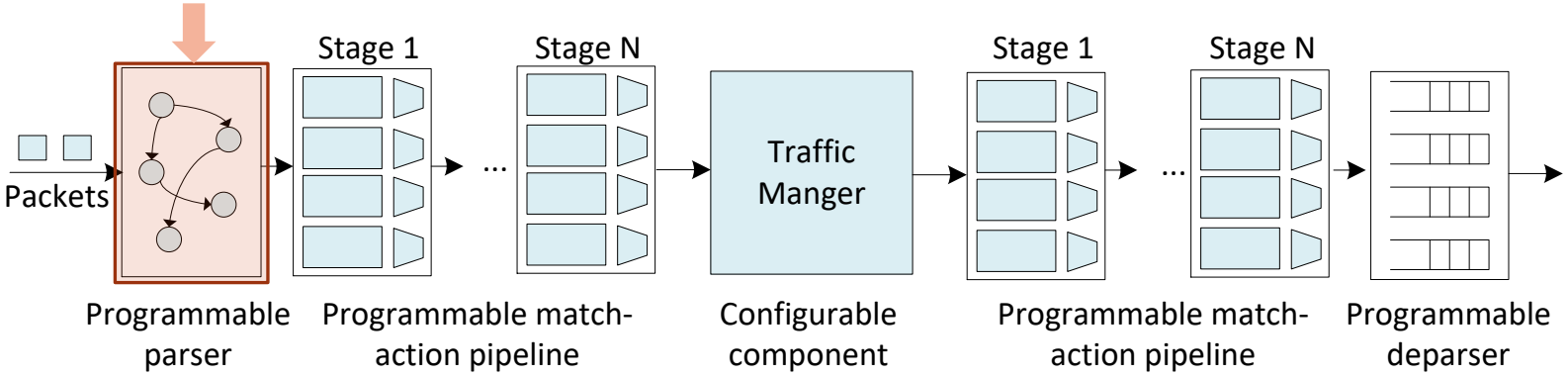
Hands on Session 2: Writing a Parser for IPv4 and IPv6

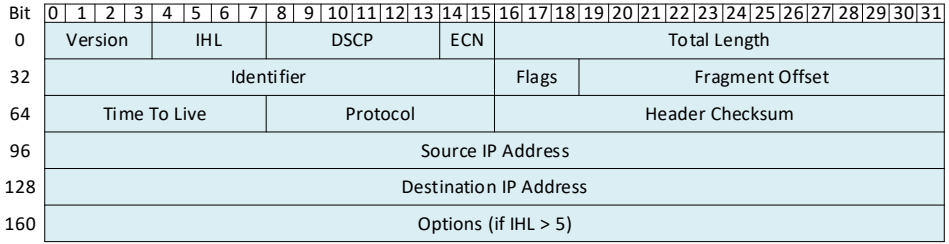
Programmable Parser

- The parser enables parsing arbitrary headers with a finite state machine
- The state machine defines the order of the headers within the packets
- The packet is split into the defined headers and the remaining is treated as the payload



Packet Headers

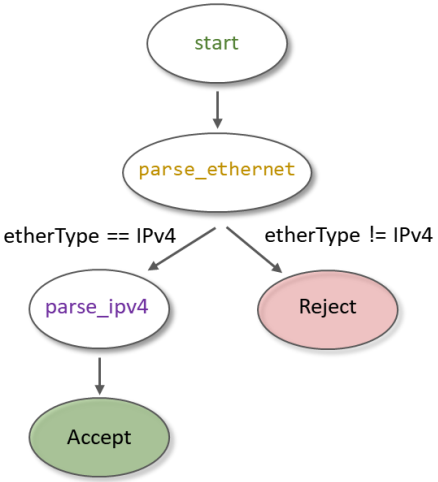
- The packet headers are specified by the programmer
- The programmer has the flexibility of defining custom/non-standardized headers
- Such capability is not available in non-programmable devices




```
header ipv4_t {
    bit<4> version;
    bit<4> ihl;
    bit<8> diffserv;
    bit<16> totalLen;
    bit<16> identification;
    bit<3> flags;
    bit<13> fragOffset;
    bit<8> ttl;
    bit<8> protocol;
    bit<16> hdrChecksum;
    ip4Addr_t srcAddr;
    ip4Addr_t dstAddr;
}
```

Programmable Parser

- The parser enables declaring arbitrary headers with a finite state machine
- The state machine defines the order of the headers within the packets

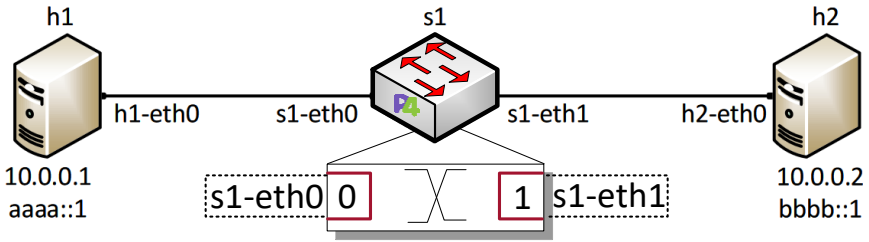



```
state start {
    transition parse_ethernet;
}
state parse_ethernet {
    packet.extract(hdr.ethernet);
    transition select(hdr.ethernet.etherType) {
        TYPE_IPV4: parse_ipv4;
        default: reject;
    }
}
state parse_ipv4 {
    packet.extract(hdr.ipv4);
    transition accept;
}
```

Lab 4: Parser Implementation

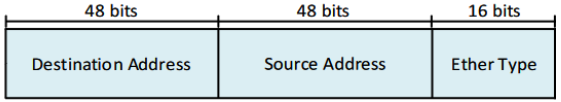
Lab Topology and Objectives

- The topology consists of two hosts: h1 and h2; one P4 switch: s1
- Defining the headers for Ethernet, IPv4 and IPv6
- Implementing the parser
- Testing and verifying the switch behavior when IPv4 and IPv6 packets are received

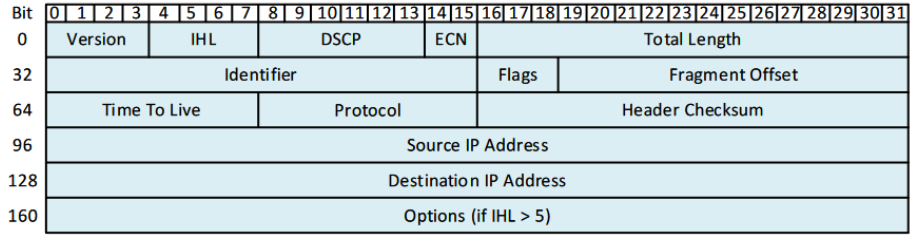


Headers Format

- Ethernet header:



- IPv4 header:



- IPv6 header: